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308 Circuits, the ninth in the 300 series of circuit design books, again contains a wide range of circuits, tips and design ideas. The book has been divided into sections, making it easy to find related subjects in a single category APPLICATIONS • Audio & HI-FI • Circuit Ideas • Computers & Microprocessors • General Interest

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308 RACING CUP ??Retour en image sur la participation des 8 voitures Peugeot Sport sur le circuit Paul Ricard.Olivier FUET, responsable compétition client ...

This is the ninth in the 300 series of circuit design books, again contains a wide range of circuits, tips and design ideas. The book has been divided into sections, making it easy to find related subjects in a single category. The book not only details DIY electronic circuits for home construction but also inspiring ideas for projects you may want to design from the ground up. Because software in general and microcontroller programming techniques in particular have become key aspects of modern electronics, a number of items in this book deal with these subjects only. Like its predecessors in the 300 series, "308 Circuits" covers the following disciplines and interest fields of modern electronics: test and measurement, radio and television, power supplies and battery chargers, general interest, computers and microprocessors, circuit ideas and audio and hi-fi.

Design of Very High-Frequency Multirate Switched-Capacitor Circuits presents the theory and the corresponding CMOS implementation of the novel multirate sampled-data analog interpolation technique which has its great potential on very high-frequency analog frond-end filtering due to its inherent dual advantage of reducing the speed of data-converters and DSP core together with the specification relaxation of the post continuous-time filtering. This technique completely eliminates the traditional phenomenon of sampled-and-hold frequency-shaping at the lower input sampling rate. Also, in order to tackle physical IC imperfections at very high frequency, the state-of-the-art circuit design and layout techniques for high-speed Switched-Capacitor (SC) circuits are comprehensively discussed: -Optimum circuit architecture tradeoff analysis -Simple speed and power trade-off analysis of active elements -High-order filtering response accuracy with respect to capacitor-ratio mismatches -Time-interleaved effect with respect to gain and offset mismatch -Time-interleaved effect with respect to timing-skew and random jitter with non-uniformly holding -Stage noise analysis and allocation scheme -Substrate and supply noise reduction -Gain-and offset-compensation techniques -High-bandwidth low-power amplifier design and layout -Very low timing-skew multiphase generation Two tailor-made optimum design examples in CMOS are presented. The first one achieves a 3-stage 8-fold SC interpolating filter with 5.5MHz bandwidth and 108MHz output sampling rate for a NTSC/PAL CCIR 601 digital video at 3 V. Another is a 15-tap 57MHz SC FIR bandpass interpolating filter with 4-fold sampling rate increase to 320MHz and the first-time embedded frequency band up-translation for DDFS system at 2.5V. The corresponding chip prototype achieves so far the highest operating frequency, highest filter order and highest center frequency with highest dynamic range under the lowest supply voltage when compared to the previously reported high-frequency SC filters in CMOS.

?Great strides have been made in the development of analog filters over the past few decades. The first book to treat these recent advances in depth, "VLSI Analog Filters" provides a comprehensive guide for researchers and upper-level graduate students, which fully prepares readers for professional work. In particular, the work covers active R filters, OTA-C filters, and switched-capacitor filters, including topics such as differential output opamps, sensitivity analysis for passive components, multiple-feedback techniques, double-sampling, and N-path filters. Throughout the book, exercises are included to reinforce understanding of concepts, and simulations are used to enhance connections to practical applications. This advanced textbook is suitable for engineering graduate students studying analog filter design, offering a full course that can feed seamlessly to employment industry. At the same time, it serves as an extremely valuable reference for researchers and engineers looking to gain a deeper understanding of the field.

Logic Synthesis and Optimization presents up-to-date research information in a pedagogical form. The authors are recognized as the leading experts on the subject. The focus of the book is on logic minimization and includes such topics as two-level minimization, multi-level minimization, application of binary decision diagrams, delay optimization, asynchronous circuits, spectral method for logic design, field programmable gate array (FPGA) design, EXOR logic synthesis and technology mapping. Examples and illustrations are included so that each contribution can be read independently. Logic Synthesis and Optimization is an indispensable reference for academic researchers as well as professional CAD engineers.

Precharge logic is used by a variety of industries in applications where processor speed is the primary goal, such as VLSI (very large systems integration) applications. Also called dynamic logic, this type of design uses a clock to synchronize instructions in circuits. This comprehensive book covers the challenges faced by designers when using this logic style, including logic basics, timing, noise considerations, alternative topologies and more. In addition advanced topics such as skew tolerant design are covered in some detail. Overall this is a comprehensive view of precharge logic, which should be useful to graduate students and designers in the field alike. It might also be considered as a supplemental title for courses covering VLSI. Comprehensive guide to precharge logic Explains both the advantages and disadvantages to help engineers decide when to utilize precharge logic Useful for engineers in a variety of industries

As the costs of power and timing become increasingly difficult to manage in traditional synchronous systems, designers are being forced to look at asynchronous alternatives. Based on reworked and expanded papers from the VII Banff Higher Order Workshop, this volume examines asynchronous methods which have been used in large circuit design, ranging from initial formal specification to more standard finite state machine based control models. Written by leading practitioners in the area, the papers cover many aspects of current practice including practical design, silicon compilation, and applications of formal specification. It also includes a state-of-the-art survey of asynchronous hardware design. The resulting volume will be invaluable to anyone interested in designing correct asynchronous circuits which exhibit high performance or low power operation.

The operational amplifier ("op amp") is the most versatile and widely used type of analog IC, used in audio and voltage amplifiers, signal conditioners, signal converters, oscillators, and analog computing systems. Almost every electronic device uses at least one op amp. This book is Texas Instruments' complete professional-level tutorial and reference to operational amplifier theory and applications. Among the topics covered are basic op amp physics (including reviews of current and voltage division, Thevenin's theorem, and transistor models), idealized op amp operation and configuration, feedback theory and methods, single and dual supply operation, understanding op amp parameters, minimizing noise in op amp circuits, and practical applications such as instrumentation amplifiers, signal conditioning, oscillators, active filters, load and level conversions, and analog computing. There is also extensive coverage of circuit construction techniques, including circuit board design, grounding, input and output isolation, using decoupling capacitors, and frequency characteristics of passive components. The material in this book is applicable to all op amp ICs from all manufacturers, not just TI. Unlike textbook treatments of op amp theory that tend to focus on idealized op amp models and configuration, this title uses idealized models only when necessary to explain op amp theory. The bulk of this book is on real-world op amps and their applications; considerations such as thermal effects, circuit noise, circuit buffering, selection of appropriate op amps for a given application, and unexpected effects in passive components are all discussed in detail. *Published in conjunction with Texas Instruments *A single volume, professional-level guide to op amp theory and applications *Covers circuit board layout techniques for manufacturing op amp circuits.

Circuit simulation has become an essential tool in circuit design and without it's aid, analogue and mixed-signal IC design would be impossible. However the applicability and limitations of circuit simulators have not been generally well understood and this book now provides a clear and easy to follow explanation of their function. The material covered includes the algorithms used in circuit simulation and the numerical techniques needed for linear and non-linear DC analysis, transient analysis and AC analysis. The book goes on to explain the numeric methods to include sensitivity and tolerance analysis and optimisation of component values for circuit design. The final part deals with logic simulation and mixed-signal simulation algorithms. There are comprehensive and detailed descriptions of the numerical methods and the material is presented in a way that provides for the needs of both experienced engineers who wish to extend their knowledge of current tools and techniques, and of advanced students and researchers who wish to develop new simulators.

The book, now in its Second Edition, presents the concepts of electrical circuits with easy-to-understand approach based on classroom experience of the authors. It deals with the fundamentals of electric circuits, their components and the mathematical tools used to represent and analyze electrical circuits. This text guides students to analyze and build simple electric circuits. The presentation is very simple to facilitate self-study to the students. A better way to understand the various aspects of electrical circuits is to solve many problems. Keeping this in mind, a large number of solved and unsolved problems have been included. The chapters are arranged logically in a proper sequence so that successive topics build upon earlier topics. Each chapter is supported with necessary illustrations. It serves as a textbook for undergraduate engineering students of multiple disciplines for a course on 'circuit theory' or 'electrical circuit analysis' offered by major technical universities across the country. SALIENT FEATURES • Difficult topics such as transients, network theorems, two-port networks are presented in a simple manner with numerous examples. • Short questions with answers are provided at the end of every chapter to help the students to understand the basic laws and theorems. • Annotations are given at appropriate places to ensure that the students get the gist of the subject matter clearly. NEW TO THE SECOND EDITION • Incorporates several new solved examples for better understanding of the subject • Includes objective type questions with answers at the end of the chapters • Provides an appendix on 'Laplace Transforms'

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